

#### 2.0A SBR<sup>®</sup> SURFACE MOUNT SUPER BARRIER RECTIFIER

#### **Features**

- Ultra Low Forward Voltage Drop
- Excellent High Temperature Capability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)

### **Mechanical Data**

- Case: SMA
- Case Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: Lead Free Plating (Matte Tin Finish.)
  Solderable per MIL-STD-202, Method 208 (3)
- Polarity Indicator: Cathode Band
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.064 grams (approximate)

SMA







**Bottom View** 

### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage	V <sub>RRM</sub> V <sub>RWM</sub>	100	V
DC Blocking Voltage	$V_{RM}$		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	71	V
Average Rectified Output Current (See Figure 1)	I <sub>O</sub>	2.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	42	А

### Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance Junction to Soldering (Note 2)	$R_{ heta}$ JS	3	
Thermal Resistance Junction to Ambient (Note 3)	$R_{ heta JA}$	119	°C/W
Thermal Resistance Junction to Ambient (Note 4)	$R_{ heta JA}$	88	
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

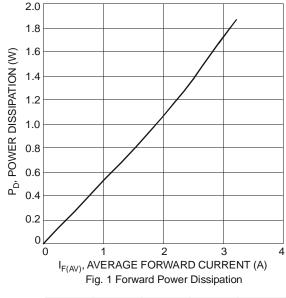
# **Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

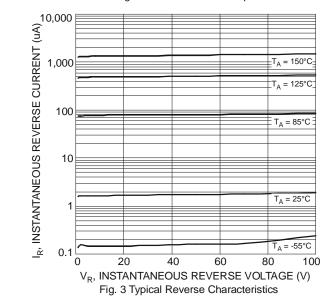
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	V <sub>(BR)R</sub>	100	-	ı	٧	$I_R = 100 \mu A$
orward Voltage Drop V <sub>F</sub>	-	0.74	0.78	\/	$I_F = 2.0A$ , $T_J = 25^{\circ}C$	
Tolward Vollage Diop	V <sub>F</sub>	-	-	0.62	V	I <sub>F</sub> = 2.0A, T <sub>J</sub> = 125°C
Leakage Current (Note 5)	-	-	0.1	mA	$V_R = 100V, T_J = 25^{\circ}C$	
	IR	-	-	5	mA	$V_R = 100V, T_J = 125^{\circ}C$

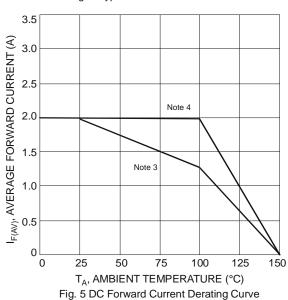
Notes:

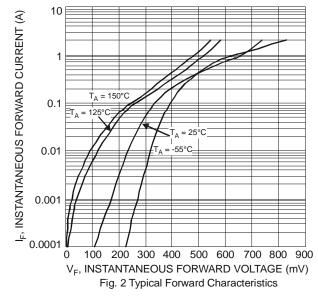
- 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.
- 2. Theoretical R<sub>US</sub> calculated from the top center of the die straight down to the PCB cathode tab solder junction.
- 3. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf. T<sub>A</sub> = 25°C
- 4. Polymide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf
- 5. Short duration pulse test used to minimize self-heating effect.

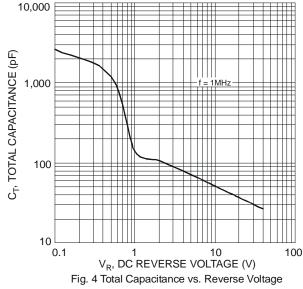


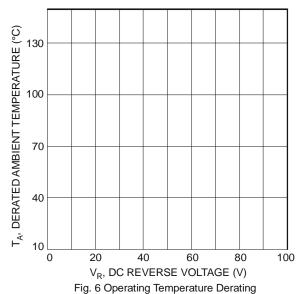












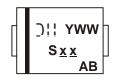


### Ordering Information (Note 6)

Part Number	Case	Packaging
SBR2A100SA-13	SMA	5000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## **Marking Information**



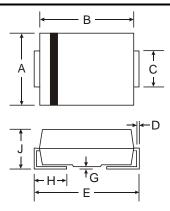
S <u>V</u> <u>B</u> or S <u>Q</u> <u>A</u> = Product Type Marking Code DII = Manufacturers' Code Marking AB = Foundry and Assembly Code

AB = Foundry and Assembly Code YM = Date Code Marking

Y = Year (ex: U = 2007)

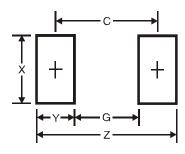
M = Month (ex: 9 = September)

## **Package Outline Dimensions**



SMA				
Dim	Min	Max		
Α	2.29	2.92		
В	4.00	4.60		
С	1.27	1.63		
D	0.15	0.31		
Е	4.80	5.59		
G	0.05	0.20		
Н	0.76	1.52		
J	2.01	2.30		
All Dimensions in mm				

## **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	6.5
G	1.5
Х	1.7
Y	2.5
С	4.0

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